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# Light Extraction Efficiency Optimization of AlGaIn-Based Deep-Ultraviolet Light-Emitting Diodes

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Using finite-difference time-domain method, the light extraction efficiency (LEE) of AlGaIn-based deep-ultraviolet light-emitting diodes (DUV LEDs) is investigated. Simulation results show that compared to flat sapphire substrate, the nano-patterned sapphire substrate (NPSS) expands the extraction angles of top surface and sidewalls. As a result, the LEE of transverse-magnetic (TM) polarized light is improved significantly. Roughening on the backside of n-AlGaIn surface significantly enhances the LEE of top surface of thin-film flip-chip DUV LEDs. However, the LEE of sidewalls of thin-film flip-chip DUV LEDs is greatly weakened. For bare DUV LED, the LEE of flip-chip LED on NPSS is estimated to be about 15%, which is around 50% higher than that of thin-film flip-chip DUV LED with roughening on the backside of n-AlGaIn surface.

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Group III nitride-based light-emitting diodes (LEDs) have a wide range of applications.<sup>1–7</sup> Among various wavelengths of LEDs, AlGaIn-based deep-ultraviolet (DUV) LEDs have attracted enormous attention for their potential applications in water purification, disinfection, sterilization, biomedicine, phototherapy, and UV curing.<sup>8–10</sup> Owing to the significant improvement in crystal quality of AlGaIn materials,<sup>11,12</sup> the hole injection efficiency,<sup>13</sup> and radiative recombination efficiency,<sup>14,15</sup> DUV LEDs with internal quantum efficiency above 60% have been reported.<sup>16,17</sup> However, the external quantum efficiency (EQE) of AlGaIn-based DUV LED is still low. For device with the wavelength around 270–280 nm, the maximum EQE of DUV LEDs is reported to be about 20%,<sup>18,19</sup> which is much lower than that of InGaIn visible LEDs.<sup>20–23</sup> Consequently, improving the light extraction efficiency (LEE) is considered to be critical to achieve high-performance AlGaIn-based DUV LEDs.

The low LEE of AlGaIn-based DUV LED is the main factor limiting the EQE. Unlike the case of visible LEDs, the low LEE of AlGaIn-based DUV LED is related to (i) the high absorption of p-GaIn layer to DUV photons,<sup>24</sup> (ii) the strong sidewall emission of AlGaIn-based DUV LED.<sup>24–26</sup> Due to a reordering of the valence energy bands (VBs) in the high-Al-content Al<sub>x</sub>Ga<sub>1-x</sub>N material, the polarization of the emitted light switches from transverse-electric (TE) to transverse-magnetic (TM) mode at a threshold Al composition.<sup>27–29</sup> It was reported that the VB ordering in AlGaIn heterostructures can be affected by the strain state and quantum confinement within the active region.<sup>30</sup> The electric field direction of TE polarized light is perpendicular to *c*-axis, whereas electric field direction of TM polarized light is parallel to *c*-axis. For AlGaIn-based DUV LED grown on *c*-(0001) sapphire substrate, TM polarized light mainly propagates in the lateral direction, resulting in a strong sidewall emission. Such an emission characteristic of AlGaIn-based DUV LEDs means that technologies improving the LEE of top surface might be less effective. The structure of the LED has a significant impact on the LEE.<sup>31–34</sup> In the vertical LED, the LEE at the roughening top surface is enhanced while the LEE at sidewall is significantly weakened due to the removal of sapphire substrate. The recent studies show that the LEE of vertical LED with roughening on the backside of III-nitride surface is comparable to that of flip-chip LED on patterned sapphire substrate.<sup>35,36</sup> This phenomenon reveals that the flip-chip DUV LED on nano-patterned sapphire substrate (NPSS) could be more effective in improving the LEE than thin-film flip-chip DUV LED with roughening on the

backside of n-AlGaIn surface, owing to the strong sidewall emission of TM-polarized light. However, the effect of chip structure on the LEE of AlGaIn-based DUV LED has not been investigated in detail.

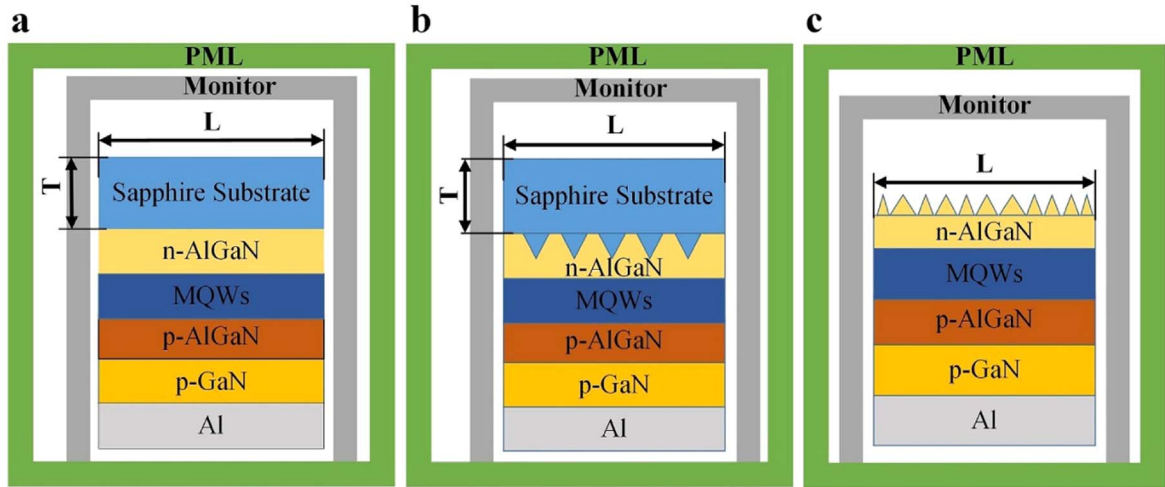
In this study, we conducted a comparative investigation on the LEE of typical structures AlGaIn-based DUV LEDs using finite-difference time-domain (FDTD) method. The LEE of the three cases of DUV LEDs were analyzed, including flip-chip DUV LED on flat sapphire substrate (FSS), flip-chip DUV LED on NPSS, and thin-film flip-chip DUV LED with roughening on the backside of n-AlGaIn surface. To investigate the LEE of flip-chip devices, we first discussed the modeling method that is applicable to DUV LEDs. Then the LEE of bare DUV LEDs for each polarization mode was calculated. The effect of NPSS on the LEE of DUV LEDs was analyzed in detail. Finally, the LEEs of flip-chip DUV LED and thin-film flip-chip DUV LED were compared.

## Methods

Figure 1 shows the simulation models of DUV LED on FSS, DUV LED on NPSS, and DUV LED with roughening on the backside of n-AlGaIn surface. The DUV LED on FSS, DUV LED on NPSS, and DUV LED with roughening on the backside of n-AlGaIn surface were denoted as DUV LED-I, DUV LED-II, and DUV LED-III, respectively. The structure parameters for the simulated flip-chip DUV LEDs were presented in Table I. The layer structure of the simulated DUV LEDs is similar to that of the reported DUV LEDs.<sup>19</sup> For DUV LED-III, the sapphire substrate was removed and the thickness of n-AlGaIn layer was reduced to be 3 μm, while the other layers are similar to flip-chip structure. For simplicity, the effects of the electrodes, area of active layer and current spreading on LEE were not considered. These simulated DUV LEDs were embedded into a cuboid simulation domain that is terminated with a perfectly matched layer (PML). The monitors were placed at a distance of 350 nm away from the simulation model. TE and TM polarized dipoles with wavelength of 280 nm were placed in the middle plane of the AlGaIn quantum-well at interval of 500 nm. The thickness and lateral dimension of sapphire substrate were set to be *T* and *L*, as shown in Fig. 1. It is indicated in Fig. 1b that the thickness of sapphire substrate did not include the height of cone-shaped pattern arrays, and the aspect ratio (*R*<sub>AR</sub>) of sapphire substrate is defined to be *T*/*L*. For cones on sapphire substrate, the height, top diameter and interval of the patterns are 0.5 μm, 0.7 μm, and 0.3 μm, respectively. For DUV LED-III, the backside of n-AlGaIn surface is roughened with cones, as shown in Fig. 1c. In DUV LED-III, the height of the cones is set to be 0.5 μm, and the bottom diameter of cones is varied from 0.5 μm to 1 μm.

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**Figure 1.** Simulation model of (a) flip-chip DUV LED on FSS (DUV LED-I), (b) flip-chip DUV LED on NPSS (DUV LED-II), and (c) thin-film flip-chip DUV LED with roughening on the backside of n-AlGaIn surface (DUV LED-III).

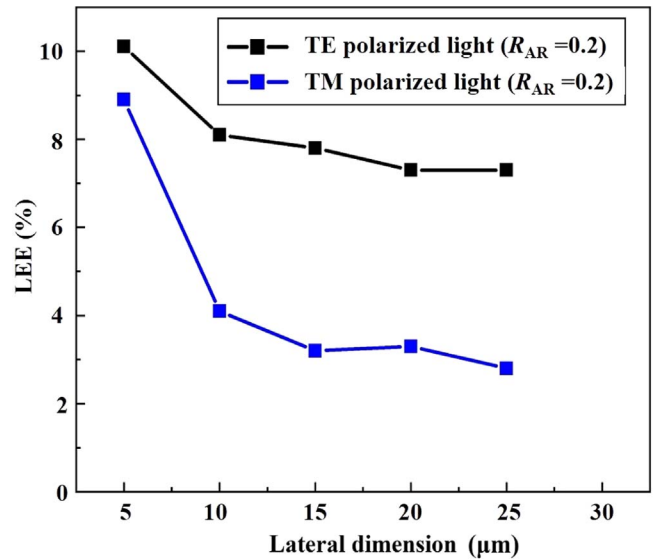
**Table I.** Parameters for each layer used in the FDTD simulations.

Layer	Thickness ( $\mu\text{m}$ )	Refractive index	Absorption coefficient ( $\text{cm}^{-1}$ )
p-GaN	0.1	2.5	170000
p-AlGaIn	0.05	2.4	10
Quantum well	0.05	2.45	1000
n-AlGaIn	3.5	2.4	10
sapphire	0-9	1.8	0

To evaluate the LEE of AlGaIn-based DUV LEDs, the simulation model should be consistent with the actual DUV LED structure. However, a simplified model modeling is necessary for the simulation in the three-dimension FDTD simulation due to the limited computational memory.<sup>37</sup> For this objective, two kinds of modeling methods are widely used to evaluate the LEE of LED. One is to use the perfect mirror layer as the simulation boundary at the sidewall of LED,<sup>38,39</sup> and the other is to use a simulation model in which the  $R_{\text{AR}}$  of the sapphire substrate is consistent with the  $R_{\text{AR}}$  of the actual LED.<sup>37</sup> Since AlGaIn-based DUV LEDs have strong sidewall emission, it is more reasonable to evaluate their LEE using the model that considers the LEE at sidewalls. Figure 2 shows the simulated LEE of DUV LED-I using a sapphire substrate with  $R_{\text{AR}}$  of 0.2. The lateral dimension of the DUV LED-I was increased from 5  $\mu\text{m}$  to 25  $\mu\text{m}$ , and the thickness of the sapphire substrate was increased from 1  $\mu\text{m}$  to 5  $\mu\text{m}$  to maintain the  $R_{\text{AR}}$  of 0.2. As the lateral dimension and thickness of sapphire substrate increased, the LEEs for both TE and TM polarized light decreased slightly, and then stabilized at approximately 7.5% and 3%, respectively. These simulation results indicated that the LEE of AlGaIn-based DUV LED can be estimated using a scaled down model whose sapphire substrate has the same  $R_{\text{AR}}$  as the actual DUV LED structure. In addition, the lateral dimension should be larger than 10  $\mu\text{m}$  to make the simulation results more accurate.

### Results and Discussion

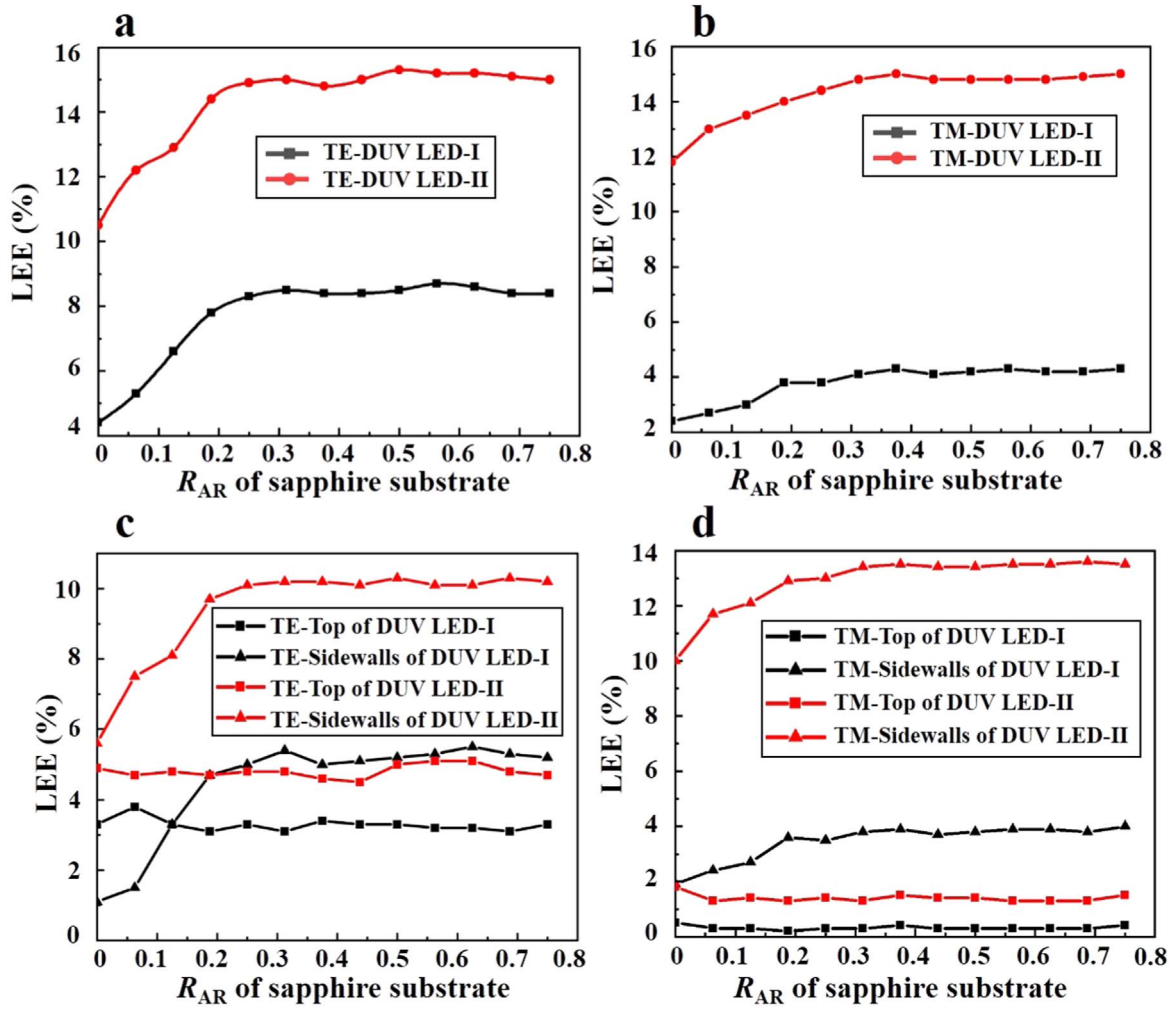
To estimate the LEE of flip-chip DUV LED, the lateral dimension of the simulation model was kept at 12.5  $\mu\text{m}$ , and the thickness of sapphire substrate was increased to extend the  $R_{\text{AR}}$  of sapphire substrate. Figure 3a shows the simulation results for TE polarized light. As the  $R_{\text{AR}}$  of sapphire substrate was extended from 0 to 0.3, the LEEs of DUV LED-I and DUV LED-II were improved significantly. When the  $R_{\text{AR}}$  was larger than 0.3, the LEE of DUV LED-I and DUV LED-II were stabilized at 8.5% and 15.1%, respectively, and a further increase in  $R_{\text{AR}}$  of sapphire substrate



**Figure 2.** The simulated LEE of DUV LED-I at different lateral dimensions. The  $R_{\text{AR}}$  of sapphire substrate of DUV LED-I was maintained at 0.2.

cannot improve the LEE of TE polarized light. In the case of  $R_{\text{AR}} > 0.3$ , the LEE of DUV LED-II for TE polarized light was improved by 78% as compared to DUV LED-I. Figure 3b illustrates the simulated LEE of TM polarized light. As the  $R_{\text{AR}}$  of sapphire substrate increased from 0 to 0.75, the LEEs of DUV LED-I and DUV LED-II were enhanced, and then stabilized at 4% and 15%, respectively. When  $R_{\text{AR}}$  of sapphire substrate was set to be 0, the LEE of DUV LED-II was enhanced by 358% as compared to DUV LED-I. However, when the  $R_{\text{AR}}$  was increased from 0 to 0.4, the LEE of DUV LED-II was improved by 27%. This result indicated that the enhanced LEE for TM polarized light strongly depended on





**Figure 3.** IEEs of DUV LED-I and DUV LED-II for (a) TE and (b) TM polarized light as a function of the  $R_{AR}$  of sapphire substrate. LEEs at top surface and sidewalls as a function of  $R_{AR}$  for (c) TE and (d) TM polarized light.

the nano-patterns of sapphire substrate rather than the increased  $R_{AR}$  of sapphire substrate. For LEDs with chip area of  $300 \times 300 \mu\text{m}^2$ , their  $R_{AR}$  varies from 0.3 to 0.5.<sup>37</sup> Consequently, the LEE of the AlGaIn-based DUV LED cannot be enhanced by further increasing the  $R_{AR}$  of sapphire substrate if the  $R_{AR}$  of sapphire substrate is larger than 0.4. A similar experimental result was reported by Lee et al.<sup>40</sup>

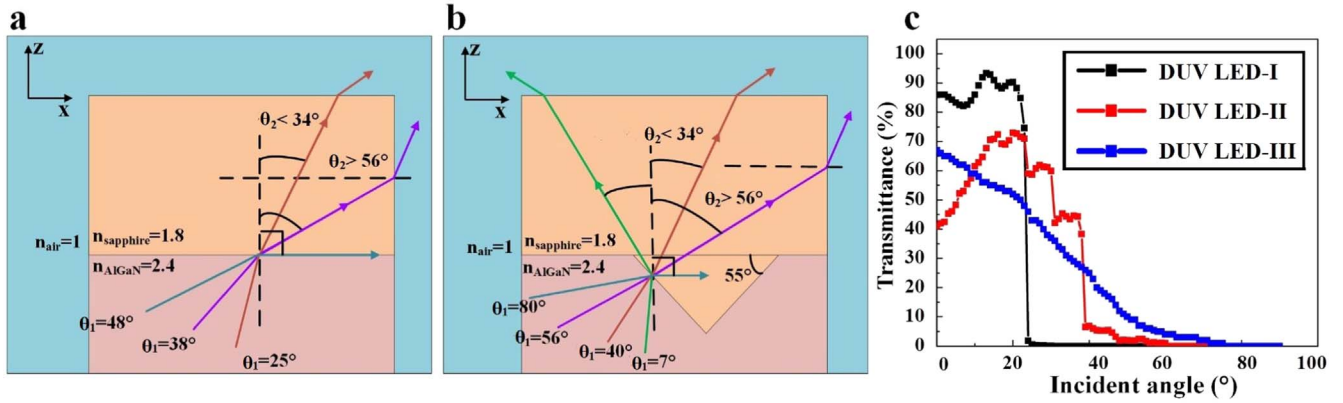
To further analyze the influence of NPSS on LEE of DUV LED, the LEEs of top surface and sidewalls were separately calculated. The chip area was maintained at  $12.5 \times 12.5 \mu\text{m}^2$ , and the  $R_{AR}$  of sapphire substrate was increased from 0 to 0.75. Figure 3c shows the LEEs of top surface and sidewalls for TE polarized light. It was found that increasing the  $R_{AR}$  of sapphire substrate only improved the LEEs at sidewalls. Compared to DUV LED-I, the LEEs of top surface and sidewalls of DUV LED-II were improved by 50% and 409%, respectively. Figure 3d shows the simulated LEE for TM polarized light. The LEE of the top surface of DUV LEDs was almost independent on the  $R_{AR}$  of sapphire substrate, as shown in Fig. 3d. Compared to DUV LED-I, the LEE of top surface of DUV LED-II of was improved by 260% while the LEE of sidewalls of DUV LED-II was enhanced by 426%. These results showed that the NPSS technology not only improves the LEE of top surface of DUV LED, but also significantly enhances the LEE of sidewalls of DUV LED.

To understand the improvement mechanism of the LEE of AlGaIn-based DUV LED, the light trajectories at AlGaIn/sapphire and sapphire/air interfaces were analyzed using Snell's equation. Using

the Bruggeman effective medium approximation,<sup>41</sup> the refractive index of cone-shaped AlGaIn/sapphire medium is about 2.2. After the DUV light with a wavelength of 280 nm propagates into the cone-shaped AlGaIn/sapphire medium, the light wavelength in cone-shaped AlGaIn/sapphire medium is shortened to be about 130 nm, which is much smaller than the size of patterns on NPSS (700 nm). Therefore, principle of geometric optics was used to estimate the extraction angles at top surface and sidewalls of DUV LED.

According to the refractive index of AlGaIn, sapphire and air, the critical angle of total internal reflection at the AlGaIn/sapphire and sapphire/air interfaces are  $\theta_{c1} = 48^\circ$  and  $\theta_{c2} = 34^\circ$ , respectively. The angles between the incident light rays or refracted light rays and the  $z$ -axis are  $\theta_1$  ( $0^\circ < \theta_1 < 90^\circ$ ) and  $\theta_2$ , respectively. Figure 4a demonstrates the typical light trajectories in DUV LED-I. Light rays refracted into FSS via flat AlGaIn/sapphire interface can be divided into three parts. Firstly, refracted light rays with  $\theta_2 < 34^\circ$  can be extracted into air from the top surface. Secondly, refracted light rays with  $34^\circ < \theta_2 < 56^\circ$  will be reflected into the AlGaIn layer and lost by means of internal absorption. Thirdly, refracted light rays with  $\theta_2 > 56^\circ$  can emit into the escape cone at sidewalls, if they are incident on the sidewalls of sapphire substrate. When the incident angles are  $25^\circ$ ,  $38^\circ$ , and  $48^\circ$ , the refraction angles at the flat AlGaIn/sapphire interface are  $34^\circ$ ,  $56^\circ$ , and  $90^\circ$ . Therefore, light rays that are incident on the flat AlGaIn/sapphire interface with  $\theta_1 < 25^\circ$  can be extracted into air from the top surface, and light rays that are incident on the flat AlGaIn/sapphire interface with  $38^\circ < \theta_1 < 48^\circ$  can be extracted into air from the sidewalls of sapphire substrate.





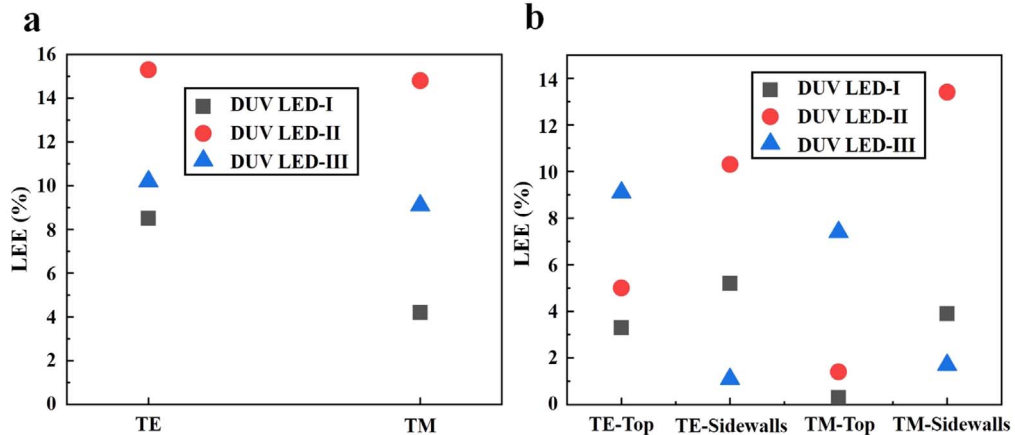
**Figure 4.** Typical light trajectories in (a) DUV LED-I and (b) DUV LED-II. (c) The simulated transmittance of three types of DUV LEDs as a function of incident angle.

Figure 4b demonstrates the typical light trajectories in DUV LED-II. The refracted light rays in NPSS are extracted in the same angle as the FSS, but the extraction angle of incident light changes significantly. According to height and diameter of the cone, the base angle of the cone is about  $55^\circ$ . When the incident angles ( $\theta_1$ ) of light are  $56^\circ$  and  $80^\circ$ , the corresponding refraction angles ( $\theta_2$ ) at cone-shaped AlGaIn/sapphire interface are  $56^\circ$  and  $90^\circ$ . It is worth noting that the incident angles of  $\theta_1 = 7^\circ$  and  $40^\circ$  at the cone-shaped AlGaIn/sapphire interface correspond to refraction angle of  $\theta_2 = 34^\circ$ , as shown by the green and brown solid lines in Fig. 4b. Therefore, light rays incident on cone-shaped AlGaIn/sapphire interface with angle of  $7^\circ < \theta_1 < 40^\circ$  can be extracted into air via the top surface, while light rays incident on cone-shaped AlGaIn/sapphire interface with angle of  $56^\circ < \theta_1 < 80^\circ$  can emit into the escape cone at sidewalls of NPSS. Compared to DUV LED-I, the extraction angles at top surface and sidewalls of DUV LED-II are extended from  $(0^\circ, 25^\circ)$  and  $(38^\circ, 48^\circ)$  to  $(7^\circ, 40^\circ)$  and  $(56^\circ, 80^\circ)$  by the cone-shaped AlGaIn/sapphire interface. Obviously, NPSS expands the range of extraction angles of top surface and sidewalls, which is helpful in extracting TM polarized light due to the larger incident angle of TM polarized light at AlGaIn/sapphire interface.<sup>42</sup>

To verify the change in extraction angle at top surface, the transmittance of three types of DUV LEDs was calculated using two-dimensional FDTD simulation, as shown in Fig. 4c. A plane wave with incident angle ranging from  $0^\circ$  to  $90^\circ$  was chosen as the incident light source. Figure 4c shows the simulated transmittance of three types of DUV LEDs. For DUV LED-I, the transmittance of light rays with incident angle  $< 25^\circ$  exceeds 80%. When the incident angle is larger than  $25^\circ$ , the transmittance of DUV LED-I is

approximately 0, indicating that the critical angle of total internal reflection in DUV LED-I was  $25^\circ$ . For DUV LED-II, the transmittance of light rays with incident angle  $< 40^\circ$  is larger than 40%. When the incident angle was larger than  $40^\circ$ , the transmittance of DUV LED-II is less than 5%. The steep drop in the transmittance of DUV LED-II at  $40^\circ$  indicated that there was strong total internal reflection in DUV LED-II. This phenomenon confirms the results obtained in Fig. 4b using geometrical optics. But for DUV LED-III, the transmittance decreases slowly as incident angle increases. Because the roughening AlGaIn/air interface scatters light into air, the extraction angle at the top surface of DUV LED-III is significantly enlarged. These simulation results reveals that the NPSS and surface texture are more efficient to extract light rays with incident angle  $> 25^\circ$  than the FSS.

To illustrate the impact of chip structure on LEE, the LEEs of three types of DUV LEDs were calculated using FDTD method. The  $R_{AR}$  of sapphire substrate used in DUV LED-I and DUV LED-II was 0.4. Figure 5a shows the simulation results of three types of DUV LED. For both TE and TM polarized light, the LEE of DUV LED-II was the highest among these DUV LEDs, and the LEE of DUV LED-III was larger than that of DUV LED-I. Figure 5b shows LEEs of DUV LEDs at top surface and sidewalls. Among the three types of DUV LEDs, the LEE of top surface of DUV LED-III was the highest, whereas the LEE of sidewalls of DUV LED-III was the lowest. The LEEs of DUV LED-II for TE and TM polarized light are 15%. The LEEs of DUV LED-III for TE and TM polarized light are around 10%. These simulation results reveal that NPSS is a more efficient technology for improving the LEE of DUV LED than roughening on the backside of n-AlGaIn surface.



**Figure 5.** (a) LEEs of DUV LED-I, DUV LED-II, and DUV LED-III for TE and TM polarization light. (b) LEEs of top surface and sidewalls of DUV LED-I, DUV LED-II, and DUV LED-III.



## Conclusions

In conclusion, we have applied FDTD method to analyze the effect of chip structure on LEE of AlGaIn-based DUV LEDs. Compared to DUV LED on FSS, the extraction angles at top surface and sidewalls of DUV LED on NPSS are extended from (0°, 25°) and (38°, 48°) to (0°, 40°) and (56°, 80°), thereby improving the LEE of top surface and sidewalls of DUV LED. For thin-film flip-chip DUV LED with roughening on the backside of n-AlGaIn surface, the LEE was enhanced at top surface but decreased at sidewalls. The NPSS extracts TM polarized light more efficiently than the roughening on the backside of n-AlGaIn surface. Consequently, the NPSS is estimated to be a more efficient technology to enhance the LEE of AlGaIn-based DUV LEDs than the roughening on the backside of n-AlGaIn surface.

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